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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

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APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10030428	01/10/2002	029	830	3729	Arbes

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## \*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/JP00/04612 07/10/2000

## \*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 11-198092 07/12/1999

JAPAN 11-198093 07/12/1999

JAPAN 11-343862 12/02/1999

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	217825US2PCT	
Verified and Acknowledged Examiners's initials			
TITLE : Method of manufacturing printed-circuit board			
U.S.DEP'T. OF COMM./PAT & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner		
		Application Examiner		
PREPARED FOR ISSUE				
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